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Views

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software up-to-date and follow the technical recommendations to help improve security. Additional security updates are planned and will be provided as they become available.

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A dual-core ARM* Cortex*-A9 MPCore*

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(HWLIB) was created to address the needs of low-level software programmers who require full access to the configuration and control facilities of SoC FPGA hardware.

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Intel's approach to Linux* for SoC FPGAs

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and the Nios® II processor is centered on upstreaming fixes and improvements of the SoC FPGA and Nios® II processor code primarily to kernel.org and DENX.de. Consequently, Intel assembled a Linux team with upstreaming as a key strategy.

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in Quartus Prime Pro Edition Design Software, Version 20.3. The Quartus Prime Pro Edition Design Software, Version 20.3 includes functional and security updates. Users should keep their software up-to-date and follow the technical recommendations to help improve security.

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Starting with Quartus v20.1 Intel® ships Arm DS for Intel® SoC FPGAs. Arm DS-5 can be used with Quartus v19.4 and earlier versions. Please refer to the following KDB Solution .

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